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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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popt

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        <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
    4. [5]Michael Barone <michael,barone@lmco.com> GPSVME fixes
    5. [6] Jean-Francois Boudreault
        <Jean-Francois.Boudreault@viagenie.qc.ca>IPv6 support
    6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option

    [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recybuf and iosignal code into separate modules.
    [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver

    9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver,
        Trimble PARSE support
   10. [11]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
11. [12]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
   12. [13]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and
       help with target configuration
   13. [14]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference
       clock driver, NT adj. residuals, integrated Greg's Winnt port.
   14. [15]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
```

- 15. [16] Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
- 16. [17]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
- 17. [18]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
- 18. [19]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite.
- 19. [20] Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
- 20. [21]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
- 21. [22]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
- 22. [23]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
- 23. [24] Hans Lambermont < Hans. Lambermont@nl.origin-it.com > or [25]<H.Lambermont@chello.nl> ntpsweep
- 24. [26]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
- 25. [27]Frank Kardel [28] < kardel (at) ntp (dot) org > PARSE < GENERIC > driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
- 26. [29]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
- 27. [30]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
- 28. [31]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
- 29. [32]George Lindholm lindholm@ucs.ubc.ca> SunOS 5.1 port
- 30. [33]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
- 31. [34] Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305

- 32. [35]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
- 33. [36]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
- 34. [37] Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
- 35. [38]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
- 36. [39]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
- 37. [40]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
- 38. [41]Derek Mulcahy <derek@toybox.demon.co.uk> and [42]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
- 39. [43]Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance
- 40. [44]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
- 41. [45]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
- 42. [46] Wilfredo S�nchez <wsanchez@apple.com> added support for NetInfo
- 44. [48]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
- 45. [49]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
- 46. [50]Michael Shields <shields@tembel.org> USNO clock driver
- 47. [51]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock
 driver
- 48. [52]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
- 49. [53] Kenneth Stone <ken@sdd.hp.com> HP-UX port
- 50. [54]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
- 51. [55]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver
- 52. [56]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
- 53. [57]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD

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sysvinit

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